Feb. 14. 2006 9:56AM   Walker & Sako, LLP   No. 1781   P. 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Uneme, Masakatsu**	
Serial No.: **10/657,464**	Group Art Unit: **2186**
Filed: **September 8, 2003**	
Title: (As Amended) **Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits**	Examiner: **Kim, Hong Chong**
Attorney Docket No.: **N26532602E**	

TRANSMITTAL COVER SHEET (37 C.F.R. §1.8(a)(i)(A))
First Class Mail

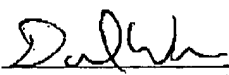Assistant Commissioner for Patents
Washington, D.C. 20231

The following is included:

[ ] Amendment/Response	Applications
[ ] After Final Rejection	[ ] Response to Missing Parts Under 37
[ ] Affidavits/Declaration(s)	C.F.R. §1.52 or §1.53
[ ] Extension of Time Request	[ ] Assignment Papers
[ ] Information Disclosure Statement	[ ] Power of Attorney/Revocation/Change
[ ] Status Request Letter	of Correspondence Address
[X] Other **Certification Under 37 C.F.R. 1.8(b)**	

<u>Conditional Petition for Extension of Time</u>
It is believed that the indicated items are timely filed. However, in the event an extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. **50-2087**.

Respectfully Submitted,

_____   February 14, 2006
Darryl G. Walker, Reg. No. 43,232   Date

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37 C.F.R. §1.8

I hereby certify that this correspondence is being

[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number: _1-571-273-8300_
    Date of Transmittal: _____February 14, 2006_____

[ ] deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.
    Date of Deposit: _____

Typed/Printed Name: **Darryl G. Walker**

Signature: _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Uneme, Masakatsu**	
Serial No.: **10/657,464**	Group Art Unit: **2186**
Filed: **September 8, 2003**	Examiner: **Kim, Hong Chong**
Title: (As Amended) **Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits**	
Attorney Docket No.: **N26532602E**	

CERTIFICATION OF PREVIOUS TRANSMISSION/DEPOSIT
37 C.F.R. 1.8(b)

Statement of <u>**Darryl G. Walker**</u>:

(1)   Examiner <u>**H. C. Kim**</u> contacted me by telephone on <u>**February 10, 2006**</u> to inform me that a faxed in response to an Office Action ("the Correspondence") dated <u>**December 16, 2005**</u> for case serial no. <u>**10/657,464**</u> was incomplete, and needed to be re-submitted.

(2)   Transmitted herewith is a copy of the previously transmitted Correspondence which includes a certification under 37 C.F.R. 1.8(a) indicating that the Correspondence was transmitted to the Patent Office on <u>**December 16, 2005**</u>.

(3)   I transmitted the Correspondence to the fax number <u>**1-571-273-8300**</u> on <u>**December 16, 2005**</u>. Included are the following:

a)   a copy of the sending unit's report confirming the transmission of the Correspondence.

b)   a copy of a first Auto-Reply Facsimile Transmission confirming the receipt of the first six pages of the Correspondence.

c)   a copy of a second Auto-Reply Facsimile Transmission confirming the receipt of the last ten pages of the Correspondence.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Signed:

_____   ___February 14, 2006_____

Darryl G. Walker   Date
Reg. No. 43,232

AMENDMENT TRANSMITTAL LETTER	Attorney Docket No: N26532602E

Application Serial Number: 10/657,464	Filing Date: 09/08/2003	Examiner: Kim, Hong Chong	Group Art Unit: 2186

Invention: **Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits**

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	20	MINUS	20	0	$50	$ 0.00
INDEP. CLAIMS	3	MINUS	3	0	$200	$ 0.00

_____ Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through _____, comprising an extension of the shortened statutory period of:

_____ one month ($120)   _____ three months ($1020)
_____ two months ($450)   _____ four months ($1590)

TOTAL ADDITIONAL FEE FOR THIS AMENDMENT	

_____ A check in the amount of **$ 0.00** is attached.

X Please charge any necessary additional fees or credit any overpayment to Deposit Account **50-2087**.

X No additional fee is required.

Conditional Petition for Additional Extension of Time
It is believed that the indicated items are timely filed. However, in the event an additional extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a).
Please charge any fees that may be required to Deposit Account No. **50-2087**.

_____December 16, 2005_____   _____
Date   **Darryl G. Walker, Reg. No. 43,232**

37 C.F.R. §1.8

I hereby certify that this correspondence is being

[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number: _____1-571-273-8300_____
       Date of Transmittal: _____December 16, 2005_____

[ ] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450.
       Date of Deposit: _____

Typed/Printed Name: _____Darryl G. Walker_____
Signature: _____

* * * Communication Result Report (Dec. 16. 2005 10:30AM) * * *

Fax Header)   Walker & Sako, LLP

Date/Time: Dec. 16. 2005 10:21AM

File No.	Mode	Destination	Pg(s)	Result	Page Not Sent
1456	Memory TX	US PTO	P. 16	OK	

Reason for error
E. 1) Hang up or line fail   E. 2) Busy
E. 3) No answer   E. 4) No facsimile connection
E. 5) Exceeded max. E-mail size

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ADMENDMENT TRANSMITTAL LETTER			Attorney Docket No: IN2653240IB

Application Serial Number: 10/657,464	Filing Date: 09/08/2003	Examiner: Kim, Hong Chong	Group Art Unit: 2186

Invention: Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	20	MINUS	20	0	250	$ 0.00
INDEP. CLAIMS	3	MINUS	3	0	3200	$ 0.00

___ Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through ___, comprising an extension of the shortened statutory period of

___ one month ($120)   ___ three months ($1020)
___ two months ($450)   ___ four months ($1590)

TOTAL ADDITIONAL FEE FOR THIS AMENDMENT | |

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X   Please charge any necessary additional fees or credit any overpayment to Deposit Account 50-2687.
X   No additional fee is required.

Conditional Petition for Additional Extension of Time
It is believed that the indicated items are timely filed. However, in the event an additional extension of time is required, please consider this a petition for extension of time under 37 C.P.R. §1.136(a).
Please charge any fees that may be required to Deposit Account No. 50-2687.

___December 16, 2005___   ___signature___
Date   Darryl G. Walker, Reg. No. 43,232

                                 H.C.T.R.A.15

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    Date of Transmittal: ___December 16, 2005___

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    Date of Deposit: _____

Typed/Printed Name: ___Darryl G. Walker___
Signature: ___signature___

Auto-Reply Facsimile Transmission

TO: Fax Sender at 14089770174

Fax Information
Date Received: 12/16/2005 1:17:45 PM [Eastern Standard Time]
Total Pages: 6 (including cover page)

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Dec. 16. 2005 9:21AM   Walker & Sako, LLP   No. 1456 P.

AMENDMENT TRANSMITTAL LETTER			Attorney Docket No: N26532601E
Application Serial Number: 10/657,464	Filing Date: 09/08/2003	Examiner: Kim, Hong Chong	Group Art Unit: 2186

Invention: Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	20	MINUS	20	0	$50	$ 0.00
INDEP. CLAIMS	3	MINUS	3	0	$200	$ 0.00

___ Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through _____, comprising an extension of the shortened statutory period of:

___ one month ($120)   ___ three months ($1020)
___ two months ($450)   ___ four months ($1590)

TOTAL ADDITIONAL FEE FOR THIS AMENDMENT

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_X_ Please charge any necessary additional fees or credit any overpayment to Deposit Account 50-2087.
_X_ No additional fee is required.

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___December 16, 2005___   _signature_
Date   Darryl G. Walker, Reg. No. 43,232

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Date of Transmittal: December 16, 2005

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Date of Deposit:

Typed/Printed Name: Darryl G. Walker
Signature: _signature_

PAGE 1/6 * RCVD AT 12/16/2005 1:17:45 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/29 * DNIS:2738300 * CSID:14089770174 * DURATION (mm-ss):01-52

Auto-Reply Facsimile Transmission

TO: Fax Sender at 14089770174

Fax Information
Date Received: 12/16/2005 1:24:51 PM [Eastern Standard Time]
Total Pages: 10 (including cover page)

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Dec. 6. 2005 10:23AV   Walker & Sako, ...P   No. 455   P. 7/6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

11. (Original) The data processing apparatus of claim 9, wherein:

each of the plurality of data processing circuits includes

a request input/output (I/O) for indicating when the data processing circuit seeks control of the semiconductor memory circuit,

5   a grant I/O for indicating when the data processing circuit is granted control of the semiconductor memory circuit, and

a busy I/O for indicating when the data processing circuit is controlling the semiconductor memory circuit.

12. (Original) The data processing apparatus of claim 11, wherein:

10   each of the plurality of data processing circuits includes

a first switch for selectively connecting the request I/O, grant I/O, and busy I/O to a requesting circuit that generates a request indication and busy indication for the data processing circuit, and

a second switch for selectively connecting the request I/O, grant

15   I/O, and busy I/O to an arbitration circuit that generates a grant indication; wherein

the first switch is disabled and the second switch is enabled when the data processing circuit is initialized as a master device, and the first switch is enabled and the second switch is disabled when the data

20   processing circuit is initialized as a slave device.

13. (Original) The data processing apparatus of claim 12, wherein:

each data processing circuit includes

a controller that generates at least one controller signal for enabling

25   or disabling the first switch and second switch according to initialization data.

14. (Original) The data processing apparatus of claim 9, wherein:

the at least one control line is directly connected to the control input of the semiconductor memory circuit and the control output of each of the plurality of

30   data processing circuits.